



**Figure 12** The circuit in Figure 11 responds (Trace B) to an asymmetrically edged pulse input (Trace A). A ramped leading edge within the LT1963A's bandwidth results in Trace B's smooth, 10-mV-p-p excursion. A fast trailing edge outside the LT1963A's bandwidth causes Trace B's abrupt 75-mV-p-p disruption. The photo intensifies the trace's latter portion for clarity.

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