



| Jitter                | 0.5V/div | 0.15V/div | Reduction (%) |
|-----------------------|----------|-----------|---------------|
| Time-interval error   | 59       | 39        | 34            |
| Period jitter         | 75       | 41        | 45            |
| Cycle-to-cycle jitter | 137      | 72        | 48            |

Figure 1 Changing the vertical resolution for this 100-MHz 1V p-p signal from 0.5V/division (left) to 0.15V/division (right) dramatically improves the measured jitter by reducing ADC quantization errors.