



Figure 1 The XNOR comparison of inputs A and B results in too much propagation delay to guarantee setup at the final flip-flop's input (a). The equivalent XNOR circuit uses NOR, AND, and OR gates (b), and OR gates and inverters realize the XNOR function (c). You can also implement the circuit using wired ORs (d) to eliminate the interlop XNOR-gate delay and almost double the usable clocking speed.