

TABLE 1 FRAMEWORK SPECIFICATIONS

Template	Core cycles/ pixel× approximately single core	Core cycles/ pixel× approximately two cores	L1 data memory required (bytes)	L2 data memory required (bytes)	Comments
Line processing	42	80	Line size×2; 1716×2 for ITU-656		Double buffering in L1
Macroblock processing	36	72	Macroblock size N×M×2	Slice of a frame; (macroblock height×line size)×4	Double buffering in L1 and L2
Interframe processing	35	70	Size of subprocessing block×number of dependent blocks	Size of subprocessing block×number of dependent blocks	Only L1 or L2 can be used, double buffering in L1 or L2