



Figure 4 An electrical diagram of a two-shared configuration uses mature pinned-photo-diode technology, arranging the photo diodes and their associated transfer-gate rows. There is one reset, one select, and one source-follower transistor for each group of pixels in two consecutive rows (a). A four-shared configuration uses mature pinned-photo-diode technology, arranging the photo diodes and their associated transfer gates in a  $2 \times 2$  configuration. There is one reset, one select, and one source-follower transistor for each group of pixels in two consecutive columns and rows (b).