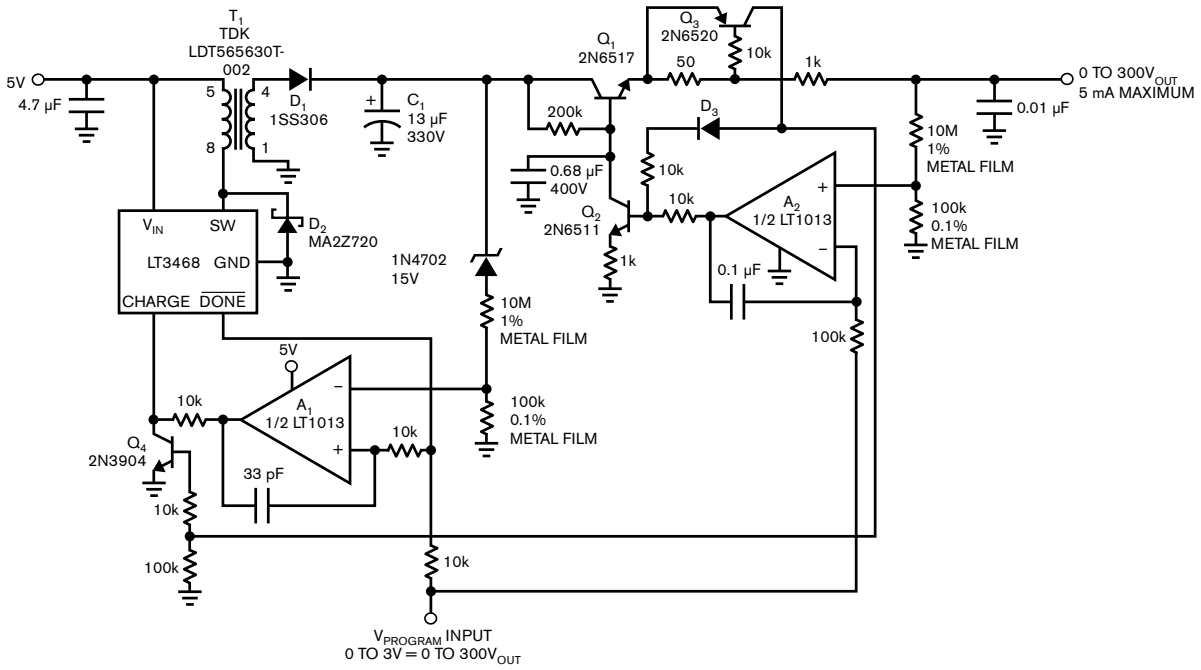

Figure 24 Adding postregulation to the circuit of Figure 22 reduces the output ripple from 2V to 2mV. An LT3468-based dc/dc converter, similar to the one in Figure 22, delivers high voltage to the collector of Q_1. Amplifier A_2 and Q_1 and Q_2 form a tracking, high-voltage linear regulator. The zener diode sets Q_1's collector-to-emitter voltage to 15V, ensuring tracking with minimal power dissipation. Transistors Q_3 and Q_4 limit the short-circuit output current.