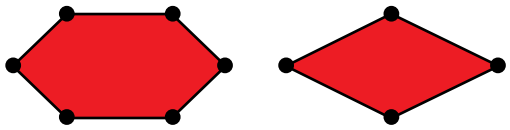

Figure 4 You can automate eye-mask tests by specifying voltage and phase values for various points on a hexagonal or diamond mask. Compare registers that connect to your SOC's on-chip analog-test bus can automatically determine whether your specified eye values match the actual values of the on-chip signals. In the six-point mask (left), the six points yield a voltage of 100 mV and a phase of 0.2 unit intervals, a voltage of 100 mV and a voltage of −0.2 unit intervals, a voltage of −100 mV and a phase of 0.2 unit intervals, a voltage of −100 mV and a phase of −0.2 unit intervals, a phase of 0.4 unit intervals, and a phase of −0.4 unit intervals. A four-point mask (right) yields a voltage of 100 mV, a voltage of −100 mV, a phase of 0.4 unit intervals, and a phase of −0.4 unit intervals.