

TABLE 1 SAR-ADC WORST-CASE SETTLING TIME

ADC Resolution (bits)	K (time-constant multiplier to 1/2-LSB accuracy)
8	6.24
10	7.62
12	9.01
14	10.4
16	11.78
18	13.17

Note: Using worst-case values, V_{IN} = full-scale voltage, or 2^N , and V_{SHD} = 0V.