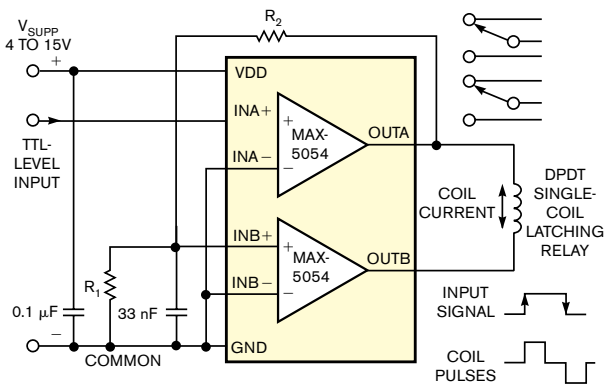


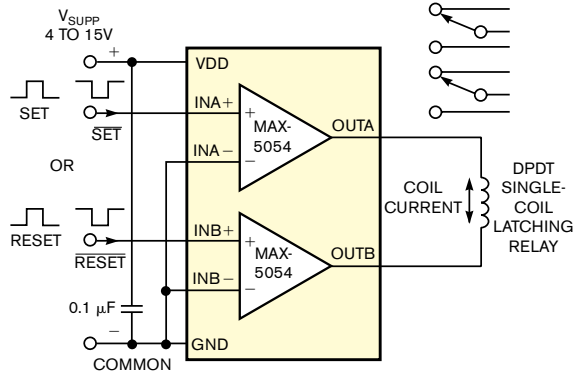
NOTE: USE A MAX5054AATA.

(a)



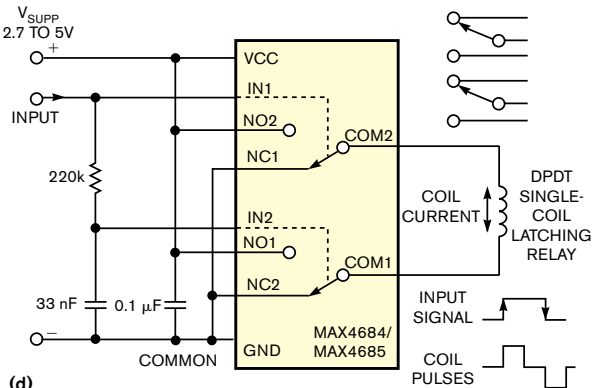
NOTES:  
USE A MAX5054BATA.  
 $R_2 = (V_R + 1) \times 220 \text{ k}\Omega$ ,  
 $R_1 = (R_2 / V_R)$ , AND  
 $V_R = [(V_{SUPP} - 2.4V) / 2.4V]$ .

(b)

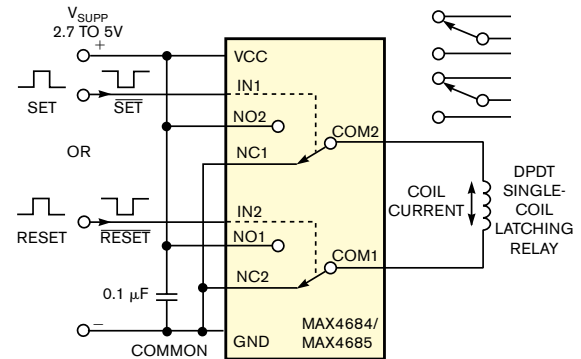


NOTES:  
IF YOU REVERSE THE LOGIC, YOU MUST DO SO FOR BOTH INPUTS.  
FOR TTL LOGIC, USE A MAX5054BATA.  
FOR CMOS LOGIC, USE A MAX5054AATA.

(c)



(d)



NOTE: IF YOU REVERSE THE LOGIC, YOU MUST DO SO FOR BOTH INPUTS.

(e)

**Figure 1** These five relay-driver circuits accommodate a variety of control signals and supply-voltage levels. One operates from CMOS-logic levels (a), and another operates from TTL levels (b). Another circuit requires two control lines to set and reset the relay (c). Two other circuits have a supply-voltage range of 2.7 to 5.5V and a maximum quiescent current of only 50 nA (d and e).