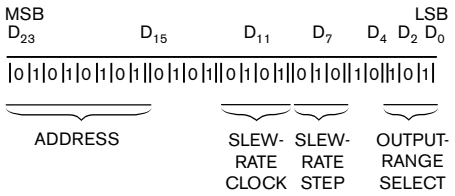

NOTE:
LSB ENTERS THE INTERNAL SHIFT REGISTER AS A LAST BIT.

Figure 2 Although the control-command sequence is at least 23 clock pulses wide, you can easily generate the alternating bit pattern.