

**TABLE 1 CMOS-GATE LEAKAGE CURRENT****NOT gate CMOS 090**

A	T= 0°	T=25°	T=50°	T=75°	T=100°
0	1,364n	3,19n	6,521n	11,98n	20,19n
1	238p	733,3p	1,895n	4,249n	8,489n

**NAND gate CMOS 090**

A	B	0°	25°	50°	75°	100°
0	0	168,5p	465,3p	1,1n	2,294n	4,327n
0	1	1,363n	3,19n	6,518n	11,97n	20,16n
1	0	1,02n	2,441n	5,089n	9,515n	16,3n
1	1	475,9p	1,466n	3,79n	8,498n	16,98

**XOR gate CMOS 090**

A	B	0°	25°	50°	75°	100°
0	0	3,405n	8,936n	20,32n	41,19n	75,98n
0	1	3,583n	9,697n	22,51n	46,24n	86,04n
1	0	2,967n	8,096n	18,95n	39,22n	73,51n
1	1	2,1n	6,512n	16,87n	37,89n	75,75n