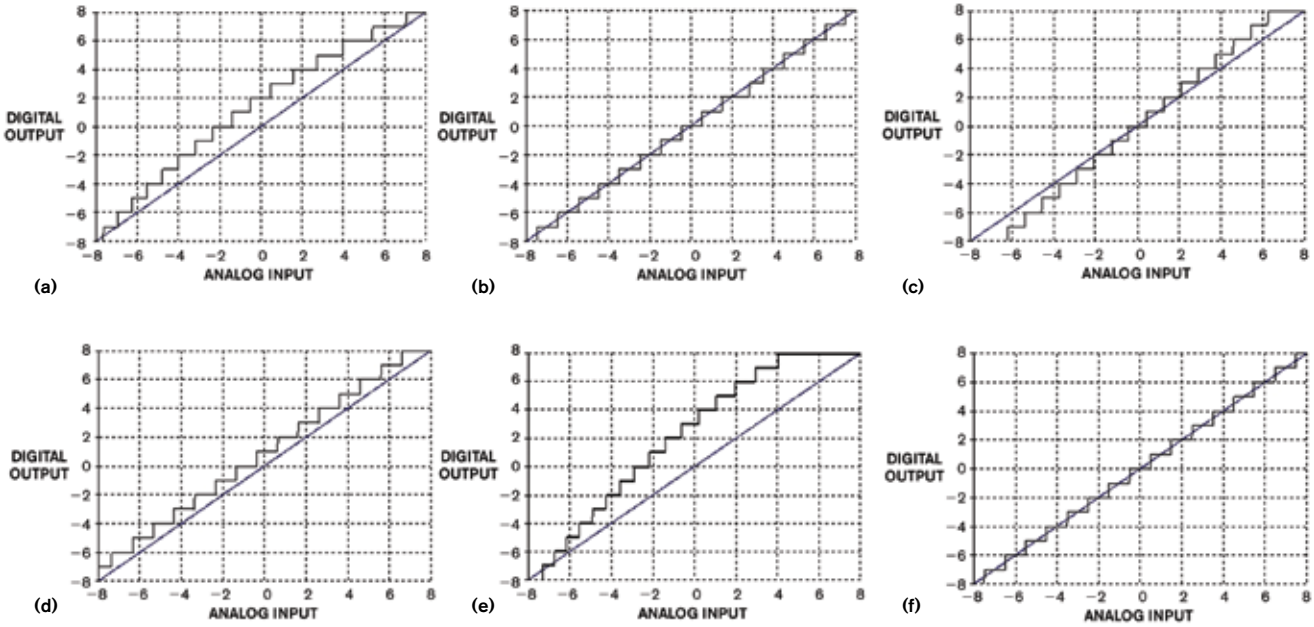

Figure 3 Combining INL error (a), DNL error (b), gain error (c), offset error (d), and total error (e) provides an understanding of the ADC in use compared with an ideal ADC (f).