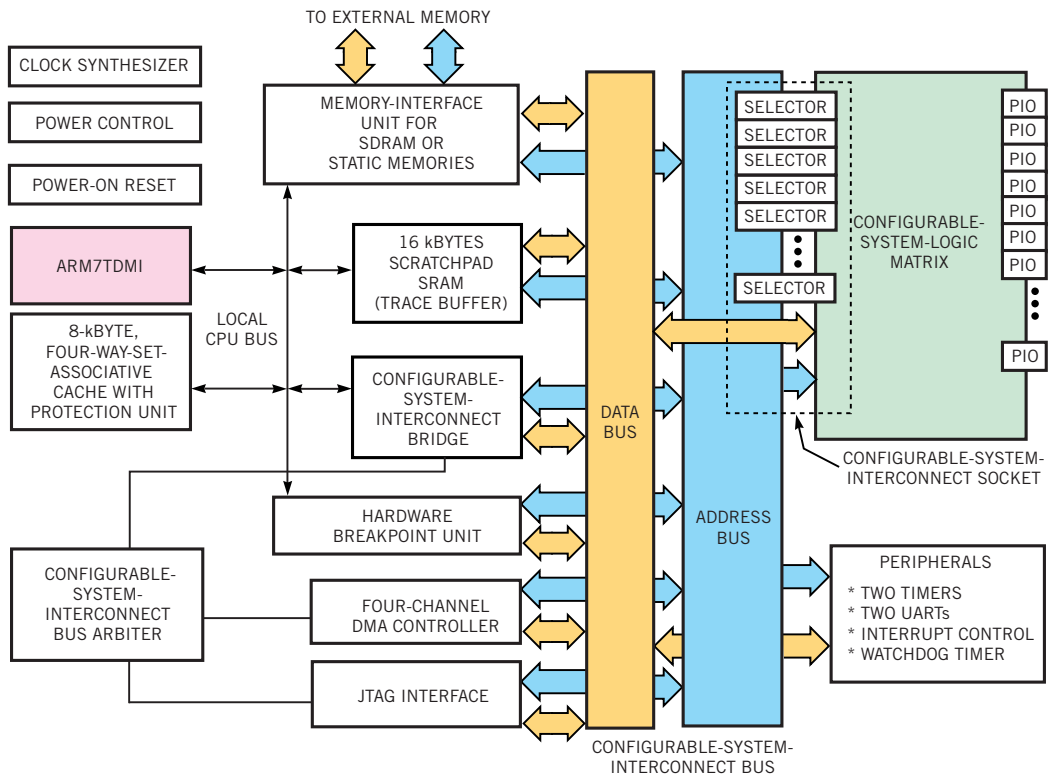


**Figure 2**



Triscend's signal-routing approach memory-maps every programmable logic array node into the embedded-processor address space.