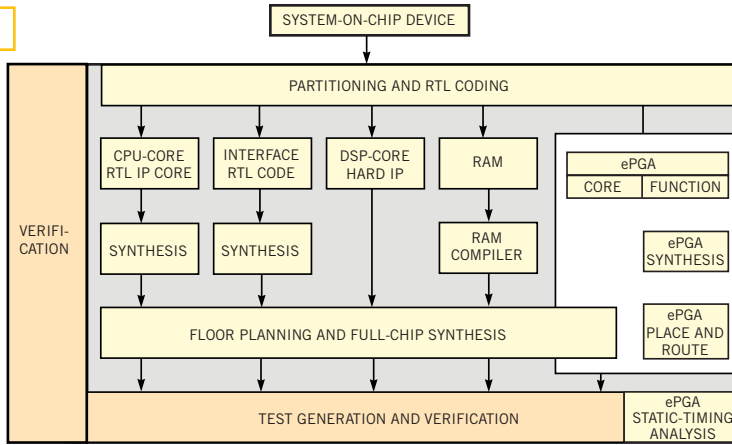


**Figure 3**



NOTE: ePGA=EMBEDDED PROGRAMMABLE ARRAY.

**Actel's embedded FPGA design-and-simulation flow incorporates a programmable-logic-tuned synthesis compiler.**