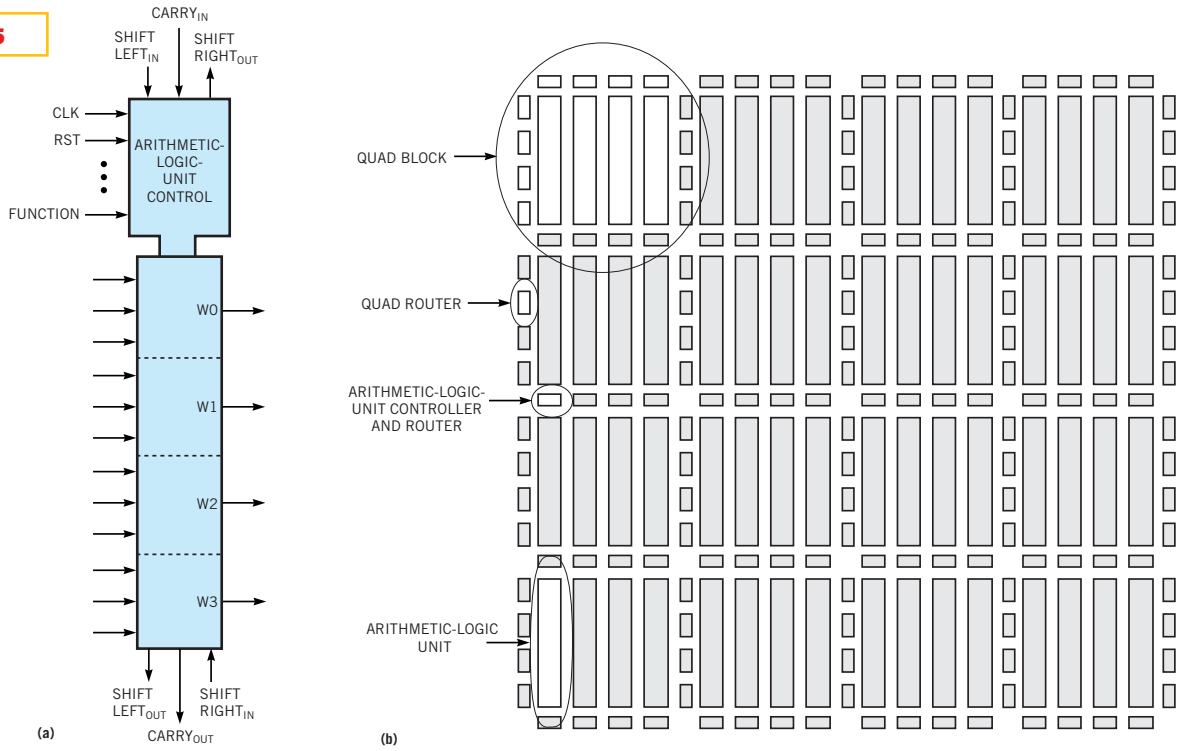


Figure 5



Adaptive Silicon's ALU-based logic cell (a) is the basis of the vendor's embedded FPGA arrays (b).