

TABLE 1—HYBRID-STANDARD-PRODUCT VENDORS, PRODUCTS, KEY FEATURES, AND REFERENCES

Vendor	Product	Key ASIC-embedded features	Issue dates and URLs for <i>EDN</i> write-ups
Altera	Excalibur	ARM and MIPS CPU cores and peripherals, AMBA interconnect, single- and dual-ported RAM blocks	Nov 30, 2000, pg 30 (www.ednmag.com/ednmag/reg/2000/11232000/leadingedge.htm#24le14)
Atmel	FPSLIC	AVR CPU core and peripherals, program and data SRAM	July 20, 2000, pg 107 (www.ednmag.com/ednmag/reg/2000/07202000/15tt.htm)
Cypress	PSI	SERDES core with SONET, Infiniband, Fibre Channel, and Gigabit Ethernet I/O options	
Lucent	OR3TP12 and OR3LP26B	Embedded 64-bit, 66-MHz PCI core	Aug 17, 2000, pg 68 (www.ednmag.com/ednmag/reg/2000/08172000/17cs_sub04.htm)
	ORLI10G	10-Gbps line interface	
	ORT4622	Four-channel, 622-Mbps, full-duplex synchronous interface with built-in CDR	Aug 17, 2000, pg 68 (www.ednmag.com/ednmag/reg/2000/08172000/17cs_sub04.htm)
	ORT82G5 ORT8850	Eight-channel; 1.25-, 2.5-, 3.125-Gbps transceiver Eight-channel, 850-Mbps, full-duplex synchronous interface with built-in CDR	Aug 17, 2000, pg 68 (www.ednmag.com/ednmag/reg/2000/08172000/17cs_sub04.htm)
QuickLogic	QuickDSP	User-configurable multiply/accumulators	Jan 20, 2000, pg 28, (www.ednmag.com/ednmag/reg/2000/01202000/leadingedge.htm#02le13)
	QuickFC	Fibre Channel encoder/decoder	Aug 17, 2000, pg 70, (www.ednmag.com/ednmag/reg/2000/08172000/17cs_sub05.htm)
	QuickPCI	32- and 64-bit PCI cores, running as fast as 66 MHz	Aug 17, 2000, pg 70 (www.ednmag.com/ednmag/reg/2000/08172000/17cs_sub05.htm)
	QuickSD	SERDES core	Oct 12, 2000, pg 22, (www.ednmag.com/ednmag/reg/2000/10122000/leadingedge.htm#21le08)
Xilinx	Virtex-II Pro	Embedded PowerPC core and peripherals, Core-Connect interconnect, SkyRail 3.125 Gbps I/O buffers	