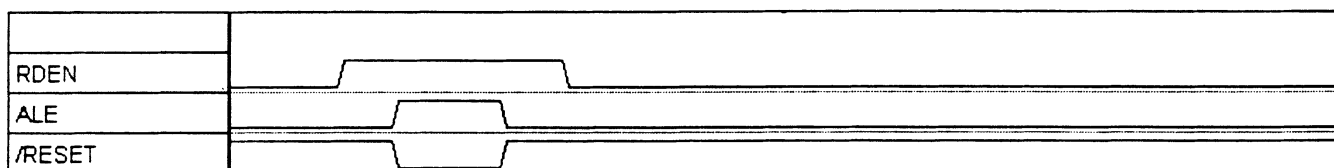
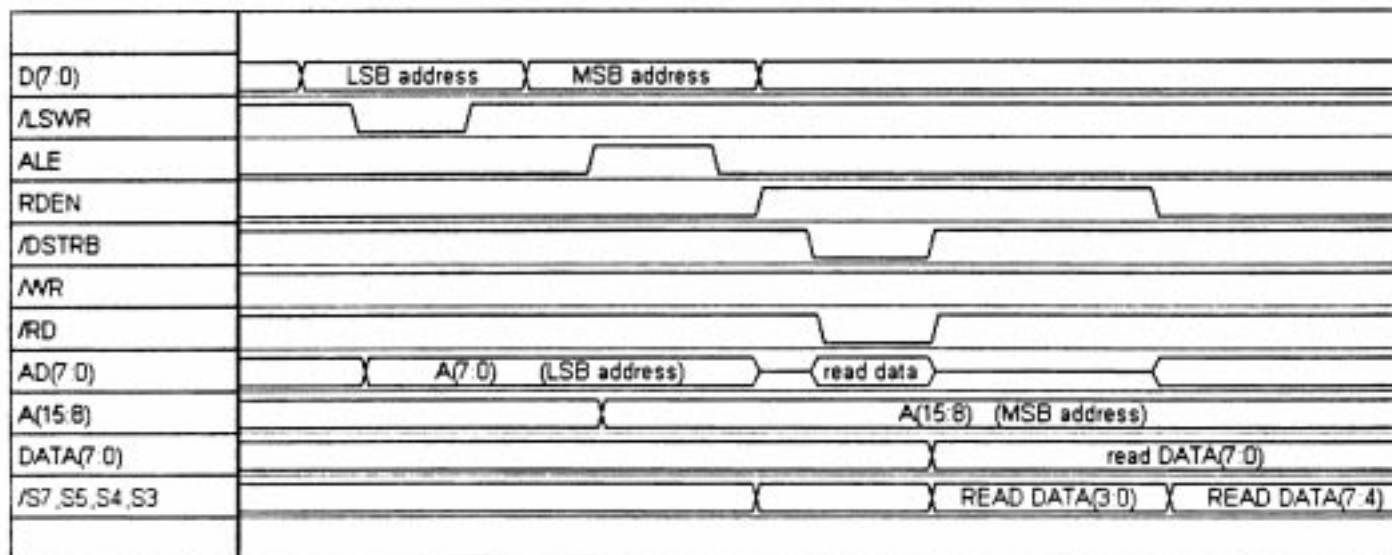
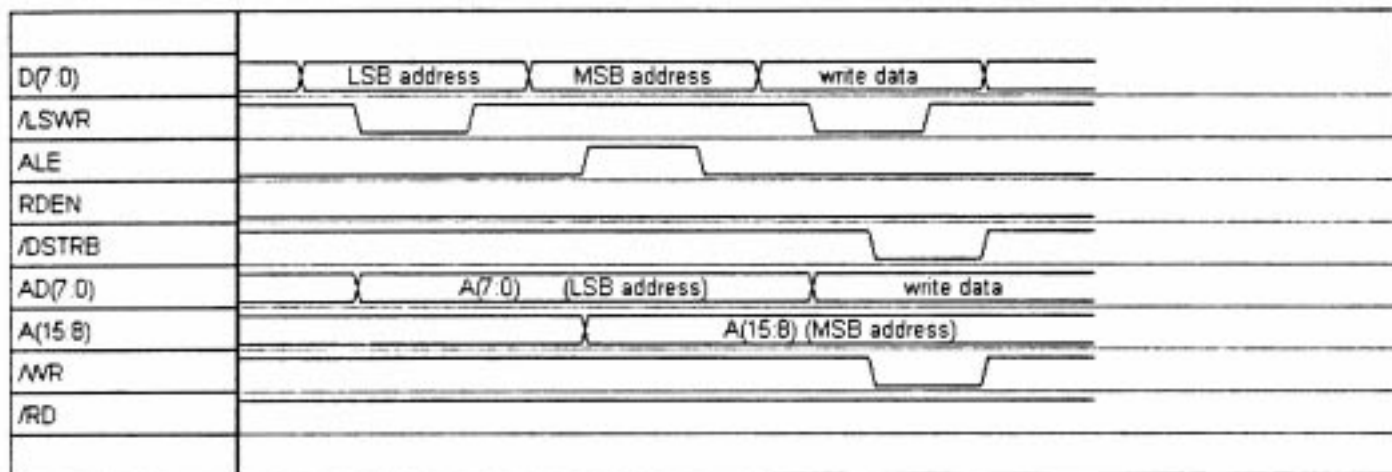


Figure 2



A PC takes over a μ C's job in controlling the write (a) and read (b) cycles in an application; an optional Reset generator (c) requires a somewhat larger CPLD.