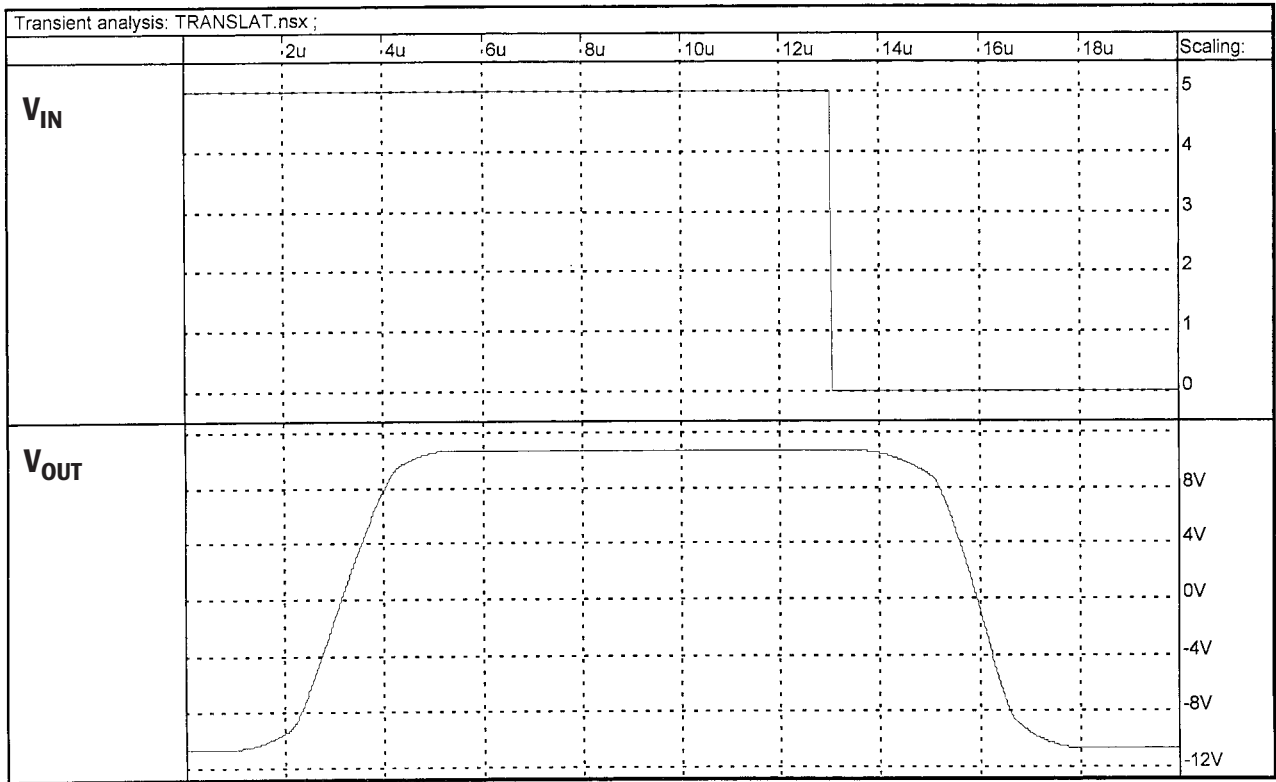


Figure 3



Input-output delay and rise and fall times are all approximately 2 μ sec in Figure 2's circuit.