Xilinx has announced availability of the Virtex-5 FPGA Gigabit Ethernet (GbE) Development Kit based on the company's latest 65 nm Virtex-5 FPGAs. Ideal for improving time-to-market of applications with Ethernet in communications, networking, industrial, scientific, medical, storage, computing, and aerospace and defense markets, the kit provides designers with a complete solution including a development board and protocol packs.

The Virtex-5 FPGA Gigabit Ethernet Development Kit enables designers to take advantage of pre-verified connectivity solutions and complete their design with confidence. The Virtex-5 LXT and SXT platform FPGAs include four built-in Tri-mode (10/100/1000 Mbps) Ethernet MACs and provide a fully integrated solution that has passed the University of New Hampshire InterOperability Lab (UNH-IOL) tests. Including integrated low-power RocketIO GTP transceiver, the Virtex-5 LXT and SXT families can support MAC+PHY in the FPGA with the Ethernet SGMII interface and a myriad of other interfaces. Xilinx also provides a path to 10 Gigabit per second Ethernet using XAUI interface.

"The flexibility and programmability of FPGAs, with integrated tri-mode Ethernet hard blocks and transceivers provide scalable Ethernet connectivity on the chip," said Anil Telikepalli, Senior Manager of Platform Solutions Marketing at Xilinx. "The complete Xilinx gigabit Ethernet solution and development kit will enable thousands of designers to adopt Gigabit Ethernet easily and allow them to focus on their value-add design."

Out-of-the-Box guarantee
The Virtex-5 FPGA Gigabit Ethernet Development Kit is part of the Xilinx solution initiative designed to allow engineers to quickly get designs up and running right out-of-the-box, and follows the PCI Express Development Kit that debuted earlier this year. The Virtex-5 FPGA Gigabit Ethernet Development Kit includes all components engineers need to develop a Virtex-5 FPGA-based Ethernet design including Virtex-5 development board (ML505) with XC5VLX50T device, RJ45 and SFP connectors, quickstart guide, resource CD, reference designs, and links to protocol pack for Ethernet, protocol-specific physical layer characterization reports, IP cores, interoperability data, and technical documentation. For more information, visit www.xilinx.com/Ethernet.

About Xilinx Virtex-5 FPGAs
Built on the industry's most advanced 65nm triple-oxide technology, ExpressFabric technology, and ASMBL architecture, the Virtex-5 family represents the fifth generation in the award-winning Virtex series of FPGAs. The Virtex-5 FPGA family offers a choice of four platforms, each delivering an optimized balance of high-performance and low-power logic, built-in serial connectivity, signal processing, and embedded processing.

Xilinx has been delivering Virtex-5 FPGAs since May 2006, and is now delivering 15 devices across three of the four platforms (LX, LXT, and SXT). Engineering samples are shipping now and may be
purchased through Xilinx distributors. Xilinx also provides ISE(TM) v9.2i design tools, protocol packs, development boards and kits that enable customers to start designing and prototyping in Virtex-5 FPGAs today. For more information, visit www.xilinx.com/virtex5.

**Pricing and availability**
The Xilinx Virtex-5 LXT FPGA Development Kit for PCI Express is immediately available for US$1,395 at www.xilinx.com/v5GbEDevKit.